

This Page Is Inserted by IFW Operations
and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

IMAGES ARE BEST AVAILABLE COPY.

As rescanning documents *will not* correct images,
Please do not report the images to the
Image Problem Mailbox.

THIS PAGE BLANK (USPTO)

GPI WEB CLIENT

[Help](#)
[Comments](#)
[Logout](#)
[Main Menu](#)
[Search Form](#)
[Posting Counts](#)
[Show S Numbers](#)
[Edit S Numbers](#)

Search Results - 16 Hits.

Term	Occurrence
(ECHO/BI (0P) (CLOCK/BI OR TIMER/BI OR TIMING/BI)) AND L11 AND DIGITAL/BI	7
ECHO/BI	331
CLOCK/BI	2673
TIMER/BI	2508
TIMING/BI	5940
ECHO/BI (0P) (CLOCK/BI OR TIMER/BI OR TIMING/BI)	57
PHASE/BI	26274
DELAY/BI	7100
CLOCK/BI	2673
TIMER/BI	2508
TIMING/BI	5940
(PHASE/BI OR DELAY/BI) (0P) (CLOCK/BI OR TIMER/BI OR TIMING/BI)	2007
MEMORY/BI	2947
DIGITAL/BI	3588

US Patents

Japanese Patents

European Patents

USOCR Patents

Database:

Refine Search:

echo/BI 0P (clock or timer or timing)/BI and L11 and digital

Search History

DB Name	Query	Hit Count	Set Name	Time
USOCR... (same as L0)		7	<u>L1</u>	Thu Mar 25 18:04:24 1999


EPO	(same as L99)	1	L0	Thu Mar 25 18:04:21 1999
JPO	echo/BI 0P (clock or timer or timing)/BI and L11 and digital	8	L99	Thu Mar 25 18:04:16 1999
USOCR	(same as L97)	8	L98	Thu Mar 25 18:03:22 1999
EPO	(same as L96)	4	L97	Thu Mar 25 18:03:20 1999
JPO	echo/BI 0P (clock or timer or timing)/BI and L11	13	L96	Thu Mar 25 18:03:15 1999
USOCR	echo/BI 0P (clock or timer or timing)/BI and memory	15	L95	Thu Mar 25 18:02:42 1999
EPO	(same as L93)	17	L94	Thu Mar 25 18:02:36 1999
JPO	echo/BI 0P (clock or timer or timing)/BI and memory	76	L93	Thu Mar 25 18:02:35 1999
EPO	(same as L91)	2	L92	Thu Mar 25 18:00:22 1999
JPO	delay 0W locked/BI 0W loop/BI and memory	1	L91	Thu Mar 25 18:00:20 1999
EPO	(same as L89)	0	L90	Thu Mar 25 17:59:58 1999
JPO	delay 0W locked/BI 0W loop/BI and l62 and memory	0	L89	Thu Mar 25 17:59:51 1999
USOCR	(same as L87)	0	L88	Thu Mar 25 17:59:24 1999
EPO	(same as L86)	0	L87	Thu Mar 25 17:59:18 1999
JPO	delay 0W locked/BI 0W loop/BI and l62 and l11	0	L86	Thu Mar 25 17:59:10 1999
USOCR	(same as L84)	0	L85	Thu Mar 25 17:58:41 1999
EPO	(same as L83)	0	L84	Thu Mar 25 17:58:37 1999
JPO	delay 0W locked/BI 0W loop/BI and l62	0	L83	Thu Mar 25 17:58:32 1999
USOCR	(same as L81)	0	L82	Thu Mar 25 17:58:25 1999
EPO	(same as L80)	0	L81	Thu Mar 25 17:58:24 1999
JPO	delay 0W locked/BI 0W loop/BI and l68	0	L80	Thu Mar 25 17:58:22 1999

USOCR	(same as L78)	0	L79	Thu Mar 25 17:58:06 1999
EPO	(same as L77)	31	L78	Thu Mar 25 17:58:06 1999
JPO	delay 0W locked/BI 0W loop/BI	26	L77	Thu Mar 25 17:58:05 1999
USOCR	(same as L75)	0	L76	Thu Mar 25 17:57:21 1999
EPO	(same as L74)	36	L75	Thu Mar 25 17:57:21 1999
JPO	delay/BI 0W locked/BI	26	L74	Thu Mar 25 17:57:20 1999
USOCR	(same as L72)	1	L73	Thu Mar 25 17:55:46 1999
EPO	(same as L71)	0	L72	Thu Mar 25 17:55:42 1999
JPO	echo/BI 0P (clock or timer or timing)/BI and L62	1	L71	Thu Mar 25 17:55:37 1999
USOCR	(same as L69)	57	L70	Thu Mar 25 17:55:20 1999
EPO	(same as L68)	175	L69	Thu Mar 25 17:55:19 1999
JPO	echo/BI 0P (clock or timer or timing)/BI	410	L68	Thu Mar 25 17:55:18 1999
USOCR	(same as L66)	22	L67	Thu Mar 25 17:54:01 1999
EPO	(same as L65)	0	L66	Thu Mar 25 17:53:58 1999
JPO	synchronize and L62	0	L65	Thu Mar 25 17:53:54 1999
USOCR	(same as L63)	49	L64	Thu Mar 25 17:53:04 1999
EPO	(same as L62)	16	L63	Thu Mar 25 17:53:02 1999
JPO	digital/BI and (clock or timer or timing)/BI and L11 and controller	46	L62	Thu Mar 25 17:52:57 1999
USOCR	(same as L60)	284	L61	Thu Mar 25 17:52:10 1999
EPO	(same as L59)	265	L60	Thu Mar 25 17:52:07 1999
JPO	digital/BI and (clock or timer or timing)/BI and L11	604	L59	Thu Mar 25 17:52:03 1999

USOCR	(same as L57)	0	L58	Thu Mar 25 17:51:46 1999
EPO	(same as L56)	0	L57	Thu Mar 25 17:51:45 1999
JPO	digital/BI and (clock or timer or timing)/BI and L1	0	L56	Thu Mar 25 17:51:44 1999
USOCR	(same as L54)	1668	L55	Thu Mar 25 17:51:25 1999
EPO	(same as L53)	6979	L54	Thu Mar 25 17:51:25 1999
JPO	digital/BI and (clock or timer or timing)/BI	16023	L53	Thu Mar 25 17:51:23 1999
USOCR	(same as L51)	789	L52	Thu Mar 25 17:51:10 1999
EPO	(same as L50)	6353	L51	Thu Mar 25 17:51:09 1999
JPO	digital/BI OP (clock or timer or timing)/BI	11888	L50	Thu Mar 25 17:51:07 1999
USOCR	(same as L48)	48	L49	Thu Mar 25 17:49:20 1999
EPO	(same as L47)	75	L48	Thu Mar 25 17:49:19 1999
JPO	synchronize/BI OP memory/BI	213	L47	Thu Mar 25 17:49:18 1999
USOCR	(same as L45)	87	L46	Thu Mar 25 17:48:10 1999
EPO	(same as L44)	10	L45	Thu Mar 25 17:48:08 1999
JPO	l11 and synchronize	41	L44	Thu Mar 25 17:48:05 1999
USOCR	(same as L42)	284	L43	Thu Mar 25 17:47:48 1999
EPO	(same as L41)	265	L42	Thu Mar 25 17:47:46 1999
JPO	l11 and digital	604	L41	Thu Mar 25 17:47:42 1999
USOCR	(same as L39)	82	L40	Thu Mar 25 17:47:25 1999
EPO	(same as L38)	84	L39	Thu Mar 25 17:47:23 1999
JPO	l11 and controller	353	L38	Thu Mar 25 17:47:20 1999

USOCR	(same as L36)	3	<u>L37</u>	Thu Mar 25 17:47:11 1999
EPO	(same as L35)	3	<u>L36</u>	Thu Mar 25 17:47:08 1999
JPO	111 and vernier	1	<u>L35</u>	Thu Mar 25 17:47:05 1999
USOCR	(same as L33)	90	<u>L34</u>	Thu Mar 25 17:46:53 1999
EPO	(same as L32)	10	<u>L33</u>	Thu Mar 25 17:46:51 1999
JPO	111 and adjust	56	<u>L32</u>	Thu Mar 25 17:46:48 1999
USOCR	(same as L30)	1	<u>L31</u>	Thu Mar 25 17:46:15 1999
EPO	(same as L29)	0	<u>L30</u>	Thu Mar 25 17:46:15 1999
JPO	synchronize and controller and vernier and adjust	0	<u>L29</u>	Thu Mar 25 17:46:14 1999
USOCR	(same as L27)	3	<u>L28</u>	Thu Mar 25 17:46:01 1999
EPO	(same as L26)	0	<u>L27</u>	Thu Mar 25 17:46:00 1999
JPO	digital and controller and vernier and adjust	0	<u>L26</u>	Thu Mar 25 17:45:59 1999
USOCR	(same as L24)	2	<u>L25</u>	Thu Mar 25 17:45:45 1999
EPO	(same as L23)	0	<u>L24</u>	Thu Mar 25 17:45:45 1999
JPO	digital and synchronize and vernier and adjust	0	<u>L23</u>	Thu Mar 25 17:45:44 1999
USOCR	(same as L21)	13	<u>L22</u>	Thu Mar 25 17:45:21 1999
EPO	(same as L20)	1	<u>L21</u>	Thu Mar 25 17:45:21 1999
JPO	digital and synchronize and controller and adjust	0	<u>L20</u>	Thu Mar 25 17:45:19 1999
USOCR	(same as L18)	1	<u>L19</u>	Thu Mar 25 17:45:07 1999
EPO	(same as L17)	0	<u>L18</u>	Thu Mar 25 17:45:06 1999
JPO	digital and synchronize and controller and vernier	0	<u>L17</u>	Thu Mar 25 17:45:06 1999

USOCR	(same as L15)	1	L16	Thu Mar 25 17:44:51 1999
EPO	(same as L14)	0	L15	Thu Mar 25 17:44:49 1999
JPO	digital and synchronize and controller and vernier and adjust	0	L14	Thu Mar 25 17:44:49 1999
USOCR	(same as L12)	468	L13	Thu Mar 25 17:43:59 1999
EPO	(same as L11)	894	L12	Thu Mar 25 17:43:56 1999
JPO	((phase or delay)/BI OP (clock or timer or timing)/BI) AND memory	3345	L11	Thu Mar 25 17:43:53 1999
USOCR	(same as L9)	2007	L10	Thu Mar 25 17:43:20 1999
EPO	(same as L8)	8555	L9	Thu Mar 25 17:43:17 1999
JPO	(phase or delay)/BI OP (clock or timer or timing)/BI	29824	L8	Thu Mar 25 17:43:14 1999
USOCR	(same as L6)	3954	L7	Thu Mar 25 17:42:49 1999
EPO	(same as L5)	8934	L6	Thu Mar 25 17:42:46 1999
JPO	(phase or delay)/BI AND (clock or timer or timing)/BI	32968	L5	Thu Mar 25 17:42:42 1999
USOCR	(same as L3)	0	L4	Thu Mar 25 17:41:10 1999
EPO	(same as L2)	1	L3	Thu Mar 25 17:41:06 1999
JPO	synchronize/BI AND memory/BI	0	L2	Thu Mar 25 17:41:05 1999
USPAT	synchronize/BI AND (memory and controller)/BI	6	L1	Thu Mar 25 17:40:17 1999

 Z39.50 Gateway Based on **CNIDR** Isite

219
GPI WEB CLIENT[Help](#)[Comments](#)[Logout](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)**Search Results - Record 1 of 1 returned.**[Generate Collection](#)

1. OCR DATA 3,500,410, Mar. 10, 1970, APPARATUS FOR ROTATING AN ANTENNA FIELD PATTERN; NAME MAY BE IN MISC FIELD, 342/356, 371 [IMAGE AVAILABLE]

1.

[Abstract](#)[Bkgrnd/Summ](#)[Cims](#)[Draw. Desc](#)[Front](#)[Full](#)[KWIC](#)[Legal](#)[Refs](#)[Cit](#)[Cis](#)[Image](#)[Generate Collection](#)

Term	Occurrence
DIGITAL/BI AND SYNCHRONIZE/BI AND CONTROLLER/BI AND VERNIER/BI	1
DIGITAL/BI	3588
SYNCHRONIZE/BI	771
CONTROLLER/BI	2326
VERNIER/BI	213

USOCR

[Show 10 More Patents](#)

Starting At: 1

Display Format: B

[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Help](#)[Comments](#)[Logout](#)

Z39.50 Gateway Based on CNIDR Isite

THIS PAGE BLANK (USPTO)

L21


GPI WEB CLIENT[Help](#)[Comments](#)[Logout](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)**Search Results - Record 1 of 1 returned.**[Generate Collection](#)

1. US004868759A , Sep. 19, 1989, Master position encoder follower system for film feeding means; ROSS, JEFFREY L (US), et al.,
INT-CL: B65B51/26; B65B57/00; B65B59/02
EUR-CL: B65B41/18

1. ☐ [Front](#) [Citation](#) [Pub.](#) [Cls.](#) [Clip Img.](#) [Full Img.](#)

[Generate Collection](#)

Term	Occurrence
DIGITAL/BI AND SYNCHRONIZE/BI AND CONTROLLER/BI AND ADJUST/BI	1
DIGITAL/BI	53538
SYNCHRONIZE/BI	1003
CONTROLLER/BI	34575
ADJUST/BI	15971

[Show 10 More Patents](#)Starting At: Display Format: [Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Help](#)[Comments](#)[Logout](#) Z39.50 Gateway Based on CNIDR Isite

THIS PAGE BLANK (USPTO)

GPI WEB CLIENT[Help](#)[Comments](#)[Logout](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)

Search Results - Records 1 through 7 of 7 returned.

[Generate Collection](#)

1. OCR DATA 3,787,863, Jan. 22, 1974, [54I RADIO ANGLE MEASUREMENT APPARATUS; Masaru Watanabe, et al., 342/424 [IMAGE AVAILABLE]

1. ☒ [Abstract](#) [Bkgnd/Summ](#) [Cims](#) [Draw. Desc](#) [Front](#) [Full](#) [KWIC](#) [Legal](#) [Refs](#) [Cit](#) [Cls](#) [Image](#)

2. OCR DATA 3,560,972, Feb. 2, 1971, APPARATUS FOR FLEXIBLY WEIGHTING RE competitive in price and superior in stabil; NAME MAY BE IN MISC FIELD, 342/162, 137, 163, 194 [IMAGE AVAILABLE]

2. ☐ [Abstract](#) [Bkgnd/Summ](#) [Cims](#) [Draw. Desc](#) [Front](#) [Full](#) [KWIC](#) [Legal](#) [Refs](#) [Cit](#) [Cls](#) [Image](#)

3. OCR DATA 3,539,978, Nov. 10, 1970, **DIGITAL** SHORT.INTERVAL RANGING APPARATUS; NAME MAY BE IN MISC FIELD, 367/108; 342/94, 135; 367/97, 901; 968/846, DIG.1 [IMAGE AVAILABLE]

3. ☐ [Abstract](#) [Bkgnd/Summ](#) [Cims](#) [Draw. Desc](#) [Front](#) [Full](#) [KWIC](#) [Legal](#) [Refs](#) [Cit](#) [Cls](#) [Image](#)

4. OCR DATA 3,537,008, Oct. 27, 1970, COMMUNICATIONS SYSTEM INCORPORATING MEANS FOR COMBATting MULTIPATH INTERFERENCE; NAME MAY BE IN MISC FIELD, 455/65 [IMAGE AVAILABLE]

4. ☐ [Abstract](#) [Bkgnd/Summ](#) [Cims](#) [Draw. Desc](#) [Front](#) [Full](#) [KWIC](#) [Legal](#) [Refs](#) [Cit](#) [Cls](#) [Image](#)

5. OCR DATA 3,508,196, Apr. 21, 1970, ERROIR DETECTION AND COIRIRECTION PEATURES; NAME MAY BE IN MISC FIELD, 714/781 [IMAGE AVAILABLE]

5. ☐ [Abstract](#) [Bkgnd/Summ](#) [Cims](#) [Draw. Desc](#) [Front](#) [Full](#) [KWIC](#) [Legal](#) [Refs](#) [Cit](#) [Cls](#) [Image](#)

6. OCR DATA 3,508,194, Apr. 21, 1970, ERROR DETECTION AND CORRECTION SYSTEM correction systems. In particular, this is NAME MAY BE IN MISC FIELD, 714/771, 781 [IMAGE AVAILABLE]

6. ☐ Abstract Bknd/Summ Cims Draw. Desc Front Full KWIC Legal Refs Cit CIs Image

7. OCR DATA 3,504,347, Mar. 31, 1970, FIGURE 1 is a block diagram of a computer system; NAME MAY BE IN MISC FIELD, 364/231.4, 231.5, 234, 236, 237, 237.2, 237.4, 238.3, 238.4, 239, 239.1, 239.7, 241.2, 248, 248.1, 248.2, 248.3, 264, 264.5, 265, 266, DIG.1 [IMAGE AVAILABLE]

7. ☐ Abstract Bknd/Summ Cims Draw. Desc Front Full KWIC Legal Refs Cit CIs Image

Generate Collection

Term	Occurrence
(ECHO/BI (0P) (CLOCK/BI OR TIMER/BI OR TIMING/BI)) AND L11 AND DIGITAL/BI	7
ECHO/BI	331
CLOCK/BI	2673
TIMER/BI	2508
TIMING/BI	5940
ECHO/BI (0P) (CLOCK/BI OR TIMER/BI OR TIMING/BI)	57
PHASE/BI	26274
DELAY/BI	7100
CLOCK/BI	2673
TIMER/BI	2508
TIMING/BI	5940
(PHASE/BI OR DELAY/BI) (0P) (CLOCK/BI OR TIMER/BI OR TIMING/BI)	2007
MEMORY/BI	2947
DIGITAL/BI	3588

USOCK

Show 10 More Patents

Starting At: 1

Display Format: B

Main Menu Search Form Posting Numbers Full S Numbers

Help

Comments

Logout

Z39.50 Gateway Based on CNDR Lite

L25

GPI WEB CLIENT[Help](#)[Comments](#)[Logout](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)**Search Results - Records 1 through 2 of 2 returned.**[Generate Collection](#)

1. OCR DATA 3,500,410, Mar. 10, 1970, APPARATUS FOR ROTATING ANTENNA FIELD PATTERN; NAME MAY BE IN MISC FIELD, 342/356, 371 [IMAGE AVAILABLE]

1. ☐ [Abstract](#) [Bkgnd/Summ](#) [Cims](#) [Draw. Desc](#) [Front](#) [Full](#) [KWIC](#) [Legal](#) [Refs](#) [C](#) [G](#) [I](#) [S](#) [T](#) [U](#) [V](#) [W](#) [X](#) [Y](#) [Z](#) [0](#) [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#)

2. OCR DATA 3,493,679, Feb. 3, 1970, PHASE SYNCHRONIZER FOR A RECEIVER; NAME MAY BE IN MISC FIELD, 375/333; 327/160, 163; 371 [IMAGE AVAILABLE]

2. ☐ [Abstract](#) [Bkgnd/Summ](#) [Cims](#) [Draw. Desc](#) [Front](#) [Full](#) [KWIC](#) [Legal](#) [Refs](#) [C](#) [G](#) [I](#) [S](#) [T](#) [U](#) [V](#) [W](#) [X](#) [Y](#) [Z](#) [0](#) [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#)

[Generate Collection](#)

Term	Occurrence
DIGITAL/BI AND SYNCHRONIZE/BI AND VERNIER/BI AND ADJUST/BI	2
DIGITAL/BI	3588
SYNCHRONIZE/BI	771
VERNIER/BI	213
ADJUST/BI	10438

USOCR

[Show 10 More Patents](#)Starting At: [Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Help](#)[Comments](#)[Logout](#)

Z39.50 Gateway Based on CNIDR List

THIS PAGE BLANK (USPTO)

L 28

GPI WEB CLIENT

Help

Comments

Logout

Main Menu

Search Form

Posting Counts

Show S Number

S

ers

Search Results - Records 1 through 3 of 3 returned.

Generate Collection

1. OCR DATA 3,531,961, Oct. 6, 1970, PHOTO AM SYSTEM FOR... NG
 vide an improved method and system for control; NAME MAY...
 FIELD, 72/8.6, 9.2, 10.4; 364/472.67, 472.67, 528.39 [IMP... A E]

1. ☐ Abstract Bknd/Summ Clms Draw. Desc Front Full KWIC Logn

2. OCR DATA 3,513,845, May 26, 1970, PHOTO HEART USE... FOR
 SYSTEM; NAME MAY BE IN MISC FIELD, 604/4; 604/47; 604/67
 AVAILABLE]

2. ☐ Abstract Bknd/Summ Clms Draw. Desc Front Full KWIC Logn

3. OCR DATA 3,500,410, Mar. 10, 1970, APPARATUS FOR ROT... ENNA
 FIELD PATTERN; NAME MAY BE IN MISC FIELD, 341/356, 371 [1... BLE]

3. ☐ Abstract Bknd/Summ Clms Draw. Desc Front Full KWIC Logn

Generate Collection

Term	Occurrence
DIGITAL/BI AND CONTROLLER/BI AND VERNIER/BI AND ADJUST/BI	3
DIGITAL/BI	3588
CONTROLLER/BI	2326
VERNIER/BI	213
ADJUST/BI	10438

Show 10 More Patents

Starting At

B

Main Menu

Search Form

Posting Counts

Show S Number

ers

[Help](#)

[Comments](#)

[Log](#)

✿ Z39.50 Gateway Based on CNIDR I.

L31

GPI WEB CLIENT

Help

Comments

Log

Main Menu

Search Form

Posting Counts

Show S Number

ers

Search Results - Record 1 of 1 returned.

Generate Report

1. OCR DATA 3,500,410, Mar. 10, 1970, APPARATUS FOR ROTARY FIELD PATTERN; NAME MAY BE IN MISC FIELD, 342/356, 371 [1]

1. PENNA
A. TITABLE

1. ☐ Abstract ☐ Bkgnd/Summ ☐ Cims ☐ Draw. Desc ☐ Front ☐ Full ☐ KWIC ☐ Legal ☐ Ref

Generate Report

Term
SYNCHRONIZE/BI AND CONTROLLER/BI AND VERNIER/BI
SYNCHRONIZE/BI
CONTROLLER/BI
VERNIER/BI
ADJUST/BI

	Occurrence
/BI	1
	771
	2326
	213
	10438

USOCR

Show 10 More Patents

Starting At:

B

Main Menu

Search Form

Posting Counts

Show S Number

ers

Help

Comments

Log

Z39.50 Gateway Based on C.A.T. 2.0

THIS PAGE BLANK (USPTO)

L35

GPI WEB CLIENT

[Help](#)[Comments](#)[Log](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show Summary](#)[Numbers](#)

Search Results - Record 1 of 1 returned.

1. JP403103787A , Apr. 30, 1994, [JP403103787A](#) (JPO)
PHASED ARRAY TYPE ULTRASONIC WAVE UNIT; TAKEDA, YASUO
INT-CL: G01S7/52; A61B8/00; G1H23/16

1 OF 1

1. ☐ [Front](#) [Citation](#) [Pub](#) [Cl](#) [Citing](#) [Full Text](#)

Term
L11 AND VERNIER/BI
PHASE/BI
DELAY/BI
CLOCK/BI
TIMER/BI
TIMING/BI
(PHASE/BI OR DELAY/BI) (OP) (CLOCK/BI OR TIMER/BI)
MEMORY/BI
VERNIER/BI

Occurrence
1
164485
67574
72122
44245
89713
29824
271314
272

JPO

[Show 10 More Patents](#) Starting At[B](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Help](#)

THIS PAGE BLANK (USPTO)

L36

GPI WEB SITE

Cor.

out S. M. 11

1. WO009825345A1, Jun. 11, 1998, U.S. Pat. No. 5,811,000
BRENT,
INT-CL: [6] H 03 K5 /00; [6] H 03 F1

1.	<input type="checkbox"/>	Front	Citation	Pub	Cls	Pl
----	--------------------------	-------	----------	-----	-----	----

2. US004789835A , Dec. 6, 1994, Automatic test systems using modular hardware (HEPTEL, ARL (S)),
INT-CL: G06F11/273
EUR-CL: G06F11/273; G06F11/273; G06F11/273; G11C7/00 15/10
H03K5/14

2. 

Front	Citation	Pub	Cls	Emp
-------	----------	-----	-----	-----

3. US004439046A , Mar. 27, 1994, Title: "A Method for HO₂ and H₂O₂ (S),
INT-CL: G04F10/00
EUR-CL: G04F10/00

3.	Front	Citation	Pub	Clas	Gr

Term	Occurrence
L11 AND VERNIER/BI	3
PHASE/BI	88076
DELAY/BI	21966
CLOCK/BI	21513
TIMER/BI	8341
TIMING/BI	20679
(PHASE/BI OR DELAY/BI) (OP) (CLOCK OR TIMER/BI)	8614
MEMORY/BI	70565
VERNIER/BI	439

EPO

[Show 10 More Patents](#)[Main Menu](#)[Search Form](#)[Posting](#)[Help](#)

Z39.50 Gateway

GPL

5

Search Form

Pe-4-6

Figure 1. Schematic representation of the experimental design. The subjects were divided into two groups: the control group (C) and the experimental group (E). The control group (C) was divided into two subgroups: the control group (C) and the control group (C). The experimental group (E) was divided into two subgroups: the experimental group (E) and the experimental group (E).

- IN

General C

USOLR

[Show 10 More Patients](#)[B](#)[Main Menu](#)[Search Form](#)[Help](#)[s](#)[Help](#)

239 50 C

L45
GPI[Help](#)[Main Menu](#)[Search Form](#)[Post](#)[S Numbers](#)

Search Results - Records 1 through 10 of 10

[Ge](#)

1. US005808596A , Sep. 15, 1998, Lig
including averaging and delaying cir
INT-CL: [6] G 09 G3/36

1. ☐ [Front](#) [Citation](#) [Pub](#) [Cls](#) [E](#)

2. US005577236A , Nov. 19, 1996,
from synchronous RAM; JOHNSON, MARK
INT-CL: [6] G 06 F12/00

2. ☐ [Front](#) [Citation](#) [Pub](#) [Cls](#) [E](#)

3. US005300864A , Apr. 5, 1994, Pro
ALLEN, JR FRANKLIN J (US),
INT-CL: [5] H05B37/00
EUR-CL: H05B37/02

3. ☐ [Front](#) [Citation](#) [Pub](#) [Cls](#) [E](#)

4. US005172396A , Dec. 15, 1992, Pu
system; ROSE, JR GEORGE D (US), et
INT-CL: H04H3/00; H04L7/00
EUR-CL: H04H3/00

4. ☐ [Front](#) [Citation](#) [Pub](#) [Cls](#) [E](#)

5. US004876700A , Oct. 24, 1989, .
(US),
INT-CL: H04L27/06
EUR-CL: H04L7/033

ces

: reading data

l system;

l cast

. ERVIN L

5. ☐ Front Citation Pub Cls Clp

6. US004506677A , Mar. 26, 1985, I:
LAMBERT, WILLIBRORDUS J S (NL),
INT-CL: A61B5/0436
EUR-CL: A61B5/0436; A61N1/08;

ulators;

6. ☐ Front Citation Pub Cls Clp

7. US004495533A , Jan. 22, 1985, I:
CHAMBERS, AARON L (US),
INT-CL: G11B27/28
EUR-CL: G11B27/28; G11B5/596

en;

7. ☐ Front Citation Pub Cls Clp

8. EP000067899A1, Dec. 29, 1982, A:
at least one integrated circuit for
signal.; FLAMM, PETER ING GRAD,
INT-CL: H04N9/46
EUR-CL: H04N9/455

prising
the FBAS

8. ☐ Front Citation Pub Cls Clp

9. US004353099A , Oct. 5, 1982, T:
and apparatus; SHUM, EDWARD K, et al
INT-CL: G11B5/02; G11B5/04
EUR-CL: G11B20/14; G11B20/18;

method

9. ☐ Front Citation Pub Cls Clp

10. US003838404A , Sep. 24, 1974, I:
CELL; HEEREN, R,
INT-CL: G11C13/00
EUR-CL: G11C11/404; G11C11/407

MEM AND

1/407

10. ☐ Front Citation Pub Cls Clp

Gen

L 45

L11 AND SYNCHRONIZE/BI
PHASE/BI
DELAY/BI
CLOCK/BI
TIMER/BI
TIMING/BI
(PHASE/BI OR DELAY/BI) (0P) (CL
MEMORY/BI
SYNCHRONIZE/BI

[Show 10 More Patents](#)[Main Menu](#) [Search Form](#)[Help](#)

Z39.50 C.

	Occurrence
	10
	88076
	21966
	21513
	8341
	20679
ING/BI)	8614
	70565
	1003

EPO

Format: [Members](#)

THIS PAGE BLANK (USPTO)

GP

Log:

Search Form

F

٧١٤١

16

1. JP401198799A, Aug. 10, 1994, LAYFIELD et al.,
KOSEKI, YUICHI, et al.,
INT-CL: G10K15/00

1. ☐ Front ☐ Citation ☐ Pub ☒ CS ☐ C

Term	Occurrence
(ECHO/BI (0P) (CLOCK/BI OR TIMER/BI) (0P) (M. /BI))	1
ECHO/BI	5831
CLOCK/BI	72122
TIMER/BI	44245
TIMING/BI	89713
ECHO/BI (0P) (CLOCK/BI OR TIMER/BI) (0P) (M. /BI)	410
DIGITAL/BI	92464
PHASE/BI	164485
DELAY/BI	67574
CLOCK/BI	72122
TIMER/BI	44245
TIMING/BI	89713
(PHASE/BI OR DELAY/BI) (0P) (M. /BI)	29824
MEMORY/BI	271314
CONTROLLER/BI	214362
CLOCK/BI	72122
TIMER/BI	44245
TIMING/BI	89713

Show 10 More

S. 1000-1001

B

THIS PAGE BLANK (USPTO)

L71

[Main Menu](#)

[Search Form](#)





[Help](#)

[C](#)

[E](#)

3/29/99 5:00 PM

THIS PAGE BLANK (USPTO)

[Main Menu](#) [Search Form](#)

[Help](#)

[About](#)

239 50

291

GG

Main Menu **Search For**

1. JP409251057A , Sep. 11, 1987, SEMICONDUCTOR MEMORY DEVICE AND METHOD OF MANUFACTURING THE SAME; TARUISHI, TOSHIO; INT-CL: G 01 R 31 /28; G 11 C 11 /00

1.	Front	Citation	Pub
----	-------	----------	-----

	Fe
(DELAY/BI (0W) LOCKED) B. (0	
DELAY/BI	
LOCKED/BI	
LOOP/BI	
DELAY/BI (0W) LOCKED) B. 0V	
MEMORY/BI	

```

=====
reference
=====
      1
=====
    7574
=====
    8541
=====
    39182
=====
      26
=====
    71314
=====

```

JP0

Show 10 More

[Main Menu](#) [Search For](#)

THIS PAGE BLANK (USPTO)

L92

GPI

Help | Contents

Main Menu Search For

Search Results - Records 1 through 2 of 2 returned

1. US005796673A , Aug. 15, 1995, Del.
 in a synchronous dynamic random access memory
 al.,
 INT-CL: [6] G 11 C 87/00

ntation
 , et

1. ☐ Front Citation Pub.

2. US005440514A , Aug. 15, 1995, writ. of
delay locked loop / S. NNAHAN, ST
 INT-CL: [6] G 11 C 87/00
 EUR-CL: G11C7/00

2. ☐ Front Citation Pub. 95

	Term
(DELAY/BI (0W) LOCKED/BI	
DELAY/BI	
LOCKED/BI	
LOOP/BI	
DELAY/BI (0W) LOCKED/BI (0W) BI	
MEMORY/BI	

ence
2
1966
8979
7576
31
0565

EPO

Show 10 E

Main Menu Search For

9730

's

L99

[Main Menu](#)[Search Form](#)[Help](#)**Search Results - Records 1 through 8 of 8 results**

1. JP407202767A , Aug. 4, 1995, E.H.
INT-CL: H 04 B3 11/00; H 04 L 1/00

1. ☐ [Front](#) [Citation](#) [Pub](#) [CS](#) [Image](#)

2. JP405031108A , Feb. 9, 1996, GENICHI,
INT-CL: A61B8/00; G06F9/06; G06F1/00

2. ☐ [Front](#) [Citation](#) [Pub](#) [CS](#) [Image](#)

3. JP402288427A , Nov. 1, 1990, INFORMATION TRANSMISSION SYSTEM;
INT-CL: H04B3/23; H04B2/03

3. ☐ [Front](#) [Citation](#) [Pub](#) [CS](#) [Image](#)

4. JP402206445A , Aug. 14, 1990, HONGO, HIRONOBU,
INT-CL: A61B8/06; A61B7/11

4. ☐ [Front](#) [Citation](#) [Pub](#) [CS](#) [Image](#)

5. JP401198799A , Aug. 14, 1989, KOSEKI, YUICHI, et al.,
INT-CL: G10K15/00

5. ☐ [Front](#) [Citation](#) [Pub](#) [CS](#) [Image](#)

6. JP363288532A , Nov. 25, 1988, SYSTEM FOR REPRODUCING CLOCK; ONO,
SHIGERU,
INT-CL: H04L7/02

6. ☐ [Front](#) [Citation](#) [Pub](#) [Cls](#) [Full Img](#)

7. JP362206931A , Sep. 11, 1987, ECHO CANCELLING SYSTEM; KOIKE,
SHINICHI,
INT-CL: H04B3/23; H04L5/14

7. ☐ [Front](#) [Citation](#) [Pub](#) [Cls](#) [Full Img](#)

8. JP358159092A , Sep. 21, 1983, PUBLIC-ADDRESS SYSTEM INCORPORATING
DELAY CIRCUIT; SAGARA, IWAO,
INT-CL: H04R3/02; H04R27/00

8. ☐ [Front](#) [Citation](#) [Pub](#) [Cls](#) [Clip Img](#) [Full Img](#)

Generate Collection

Term	Occurrence
(ECHO/BI (0P) (CLOCK/BI OR TIMER/BI OR TIMING/BI)) AND L11 AND DIGITAL/BI	8
ECHO/BI	5831
CLOCK/BI	72122
TIMER/BI	44245
TIMING/BI	89713
ECHO/BI (0P) (CLOCK/BI OR TIMER/BI OR TIMING/BI)	410
PHASE/BI	164485
DELAY/BI	67574
CLOCK/BI	72122
TIMER/BI	44245
TIMING/BI	89713
(PHASE/BI OR DELAY/BI) (0P) (CLOCK/BI OR TIMER/BI OR TIMING/BI)	29824
MEMORY/BI	271314
DIGITAL/BI	92464

Show 10 More Patents

Starting At: 1

Display Format: B

[Main Menu](#)

[Search Form](#)


[Posting Counts](#)

[Show S Numbers](#)

[Edit S Numbers](#)

L99

Help	Comments	Logout
------	----------	--------

 Z39.50 Gateway Based on **CNDR** Isite

THIS PAGE BLANK (LSPD)

2100

GPI WEB CLIENT

[Help](#)
[Comments](#)
[Logout](#)
[Main Menu](#)
[Search Form](#)
[Posting Counts](#)
[Show S Numbers](#)
[Edit S Numbers](#)

Search Results - Record 1 of 1 returned.

[Generate Collection](#)

1. GB002161932A , Jan. 22, 1986, Acoustic distance sensing system;
 HANKS, PAUL, et al.,
 INT-CL: [4] G01S15/08
 EUR-CL: G01S15/14; G01S7/529

1. [Front](#) [Citation](#) [Pub](#) [Cls](#) [Clip Img](#) [Fulling](#)

[Generate Collection](#)

Term	Occurrence
(ECHO/BI (0P) (CLOCK/BI OR TIMER/BI OR TIMING/BI)) AND L11 AND DIGITAL/BI	1
ECHO/BI	3746
CLOCK/BI	21513
TIMER/BI	8341
TIMING/BI	20679
ECHO/BI (0P) (CLOCK/BI OR TIMER/BI OR TIMING/BI)	175
PHASE/BI	88076
DELAY/BI	21966
CLOCK/BI	21513
TIMER/BI	8341
TIMING/BI	20679
(PHASE/BI OR DELAY/BI) (0P) (CLOCK/BI OR TIMER/BI OR TIMING/BI)	8614
MEMORY/BI	70565
DIGITAL/BI	53538

[Show 10 More Patents](#)


 Starting At:

 Display Format:
[Main Menu](#)
[Search Form](#)
[Posting Counts](#)
[Show S Numbers](#)
[Edit S Numbers](#)

[Help](#)

[Comments](#)

[Logout](#)

 **Z39.50 Gateway Based on CNIDR Isite**

L101

GPI WEB CLIENT[Help](#)[Comments](#)[Logout](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)

Search Results - Records 1 through 7 of 7 returned.

[Generate](#)[Section](#)

1. OCR DATA 3,787,863, Jan. 22, 1974, [541 RADIO ANGLE MEASUREMENT APPARATUS; Masaru Watanabe, et al., 342/404 [IMAGE AVAILABLE]

1. ☐ [Abstract](#) [Bkgn'd/Summ](#) [Cims](#) [Draw. Desc](#) [Front](#) [Full](#) [KWIC](#) [Legal](#) [Refs](#) [Cit](#) [Cis](#) [Image](#)

2. OCR DATA 3,560,972, Feb. 2, 1971, APPARATUS FOR FLEXIBLY WEIGHTING RE competitive in price and superior in stabil; NAME MAY BE IN MISC FIELD, 342/162, 137, 163, 194 [IMAGE AVAILABLE]

2. ☐ [Abstract](#) [Bkgn'd/Summ](#) [Cims](#) [Draw. Desc](#) [Front](#) [Full](#) [KWIC](#) [Legal](#) [Refs](#) [Cit](#) [Cis](#) [Image](#)

3. OCR DATA 3,539,978, Nov. 10, 1970, DIGITAL SHORT-INTERVAL RANGING APPARATUS; NAME MAY BE IN MISC FIELD, 342/108; 342/94, 135; 367/97, 901; 968/846, DIG.1 [IMAGE AVAILABLE]

3. ☐ [Abstract](#) [Bkgn'd/Summ](#) [Cims](#) [Draw. Desc](#) [Front](#) [Full](#) [KWIC](#) [Legal](#) [Refs](#) [Cit](#) [Cis](#) [Image](#)

4. OCR DATA 3,537,008, Oct. 27, 1970, COMMUNICATIONS SYSTEM INCORPORATING MEANS FOR COMBATTING MULTIPLE INTERFERENCE; NAME MAY BE IN MISC FIELD, 455/65 [IMAGE AVAILABLE]

4. ☐ [Abstract](#) [Bkgn'd/Summ](#) [Cims](#) [Draw. Desc](#) [Front](#) [Full](#) [KWIC](#) [Legal](#) [Refs](#) [Cit](#) [Cis](#) [Image](#)

5. OCR DATA 3,508,196, Apr. 21, 1970, ENCAPSULATION AND CO-RECTION FEATURES; NAME MAY BE IN MISC FIELD, 771 [IMAGE AVAILABLE]

5. ☐ [Abstract](#) [Bkgn'd/Summ](#) [Cims](#) [Draw. Desc](#) [Front](#) [Full](#) [KWIC](#) [Legal](#) [Refs](#) [Cit](#) [Cis](#) [Image](#)

6. OCR DATA 3,508,194, Apr. 21, 1970, SYSTEM correction systems. fn particular, FIELD, 714/771, 781 [IMAGE AVAILABLE] CORRECTfON BE IN MISC

6. ☐ Abstract ☐ Bkgnd/Summ ☐ Cims ☐ Draw. Desc ☐ Frnt ☐ Cls ☐ Image

7. OCR DATA 3,504,347, Mar. 31, 1970, diagram of a computer system; NAME MAY BE IN MISC FIELD, 304/231.4, 231.5, 234, 236, 237, 237.2, 237.4, 238.3, 238.4, 239, 239.7, 241.2, 248, 248.1, 248.2, 248.3, 264, 264.5, 265, 266, DIG

7. ☐ Abstract ☐ Bkgnd/Summ ☐ Cims ☐ Draw. Desc ☐ Frnt ☐ Cls ☐ Image

Generat. 11/11/11

Term	Occurrence
(ECHO/BI (0P) (CLOCK/BI OR TIMER/BI) DIGITAL/BI	7
ECHO/BI	331
CLOCK/BI	2673
TIMER/BI	2508
TIMING/BI	5940
ECHO/BI (0P) (CLOCK/BI OR TIMER/BI C	57
PHASE/BI	26274
DELAY/BI	7100
CLOCK/BI	2673
TIMER/BI	2508
TIMING/BI	5940
(PHASE/BI OR DELAY/BI) (0P) (CLOCK/BI	2007
MEMORY/BI	2947
DIGITAL/BI	3588

450LR

Show 10 More Patents

Play Format: B

Main Menu Search Form Pat

Pat S Numbers

Help

Z39.50 Gate

WEST 1.0[Help](#)[Main Menu](#) [Search Form](#) [Posting Counts](#) [Show WS Numbers](#) [Edit WS Numbers](#)**Search Results - /**

Term	Documents
((memory and (phase or delay)) and vernier and digital and controller)	1

Database: [Perwent](#)[Refine Search:](#)

12 and vernier and digital and controller

Search History

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
DWPI	memory and phase	10056	<u>L1</u>
DWPI	memory and (phase or delay)	21528	<u>L2</u>
DWPI	memory and (phase or delay) and (clock or timing)	4790	<u>L3</u>
DWPI	memory and (phase or delay) and (clock or timing) and digital	1547	<u>L4</u>
DWPI	memory and (phase or delay) and (clock or timing) and adjust	195	<u>L5</u>
DWPI	memory and (phase or delay) and (clock or timing) and controller	509	<u>L6</u>
DWPI	digital and adjust and synchronize and controller	0	<u>L7</u>
DWPI	adjust and digital and controller	762	<u>L8</u>
DWPI	adjust and digital and synchronize	8	<u>L9</u>
DWPI	adjust and controller and synchronize	10	<u>L10</u>
DWPI	digital and controller and synchronize	26	<u>L11</u>
DWPI	l8 or l9 or l10 or l11	806	<u>L12</u>
DWPI	l3 and l12	6	<u>L13</u>
DWPI	memory and l12	165	<u>L14</u>
DWPI	l2 and l12	24	<u>L15</u>
DWPI	l2 and l12 and vernier	0	<u>L16</u>
DWPI	l2 and l12 and initial	3	<u>L17</u>
DWPI	l2 and l12 and echo	0	<u>L18</u>
DWPI	l2 and echo	216	<u>L19</u>
DWPI	l2 and echo and vernier	0	<u>L20</u>
DWPI	l2 and vernier	17	<u>L21</u>
DWPI	l2 and vernier and digital	6	<u>L22</u>
DWPI	l2 and vernier and digital and synchronize	0	<u>L23</u>
DWPI	memory and vernier and digital and synchronize	0	<u>L24</u>
DWPI	l2 and vernier and synchronize	0	<u>L25</u>
DWPI	l2 and vernier and digital and controller	1	<u>L26</u>

WEST 1.0[Help](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show WS Numbers](#)[Edit WS Numbers](#)

Search Results - Record(s) 1 through 8 of 8 returned.

1. Document ID: JP 06204944 A US 5557647 A,
Relevance Rank: 99

DERWENT-ACC-NO: 1994-275049
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Baseband signal demodulator for receiver - has sampling clock
generation device which changes phase of system clock based on phase error
data to generate optimum sampling clock for baseband signal

Inventor Name KUSHIGE, N

PRIORITY-DATA: 1993JP-0000794 (January 6, 1993) , 1993JP-0004239 (January
13, 1993)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 06204944 A	July 22, 1994	N/A	011	H04B 007/26
US 5557647 A	September 17, 1996	N/A	024	H04L 007/00

INT-CL: H04B007/26 H04J003/00 H04J003/06 H04L007/00 H04L027/22

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

2. Document ID: DE 59108397 G EP 479268 A DE
4038561 A DE 4038561 C2 EP 479268 A3 EP
479268 B1,
Relevance Rank: 92

DERWENT-ACC-NO: 1992-116034
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Clocked correlation and signal processing system for TDM receiver -
identifies required signal sequence from obtained correlation values

Inventor Name HOENIG, J

PRIORITY-DATA: 1990DE-4038561 (December 4, 1990) , 1990DE-4031124 (October
2, 1990)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
DE 59108397 G	January 16, 1997	N/A	000	H04L 007/04
EP 479268 A	April 8, 1992	N/A	025	N/A
DE 4038561 A	April 9, 1992	N/A	018	N/A
DE 4038561 C2	April 22, 1993	N/A	018	H04J 003/06
EP 479268 A3	June 9, 1993	N/A	000	N/A
EP 479268 B1	December 4, 1996	G	027	H04L 007/04

INT-CL: H04J003/06 H04L003/06 H04L007/04

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

3. Document ID: GB 2183974 A GB 2183974 B JP
62163441 A US 4773083 A,

Relevance Rank: 91

DERWENT-ACC-NO: 1987-159445
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title QPSK demodulator e.g. for satellite communication system - comprises
phase shifter or delay line responsive to error signal along with corresp.
dither clock

Inventor Name BAUMBACH, R L

PRIORITY-DATA: 1985US-0796342 (November 8, 1985)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
GB 2183974 A	June 10, 1987	N/A	000	N/A
GB 2183974 B	October 4, 1989	N/A	000	N/A
JP 62163441 A	July 20, 1987	N/A	000	N/A
US 4773083 A	September 20, 1988	N/A	014	N/A

INT-CL: H04B007/15 H04L027/22

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

4. Document ID: EP 302262 A DE 3855492 G US
5086500 A EP 302262 B1,

Relevance Rank: 89

DERWENT-ACC-NO: 1989-040448
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Reduced instruction set computer risc - uses discrete integrated circuit with constant impedance transmission lines and adjustable delayed clocks to ease heat dissipation

Inventor Name GREUB, H J

PRIORITY-DATA: 1987US-0084003 (August 7, 1987) , 1989US-0449445 (December 12, 1989)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
EP 302262 A	February 8, 1989	E	017	N/A
DE 3855492 G	October 2, 1996	N/A	000	G06F 001/04
US 5086500 A	February 4, 1992	N/A	000	N/A
EP 302262 B1	August 28, 1996	E	032	G06F 001/04

INT-CL: G06F001/04 G06F013/00 G06F015/00 H05K001/00

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

5. Document ID: US 5809064 A EP 552975 A2 AU
9331971 A CA 2087909 A US 5390207 A CN
1080059 A EP 552975 A3 AU 660757 B US
5495499 A SG 43672 A1 US 5734674 A,

Relevance Rank: 89

DERWENT-ACC-NO: 1993-236645
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Receiver for signalling environments with multipath fading -
demodulates and decodes composite radio frequency signal consisting of
several transmitted pseudorandom noise encoded signals using RF
down-converter giving composite IF signal

Inventor Name FENTON, P

PRIORITY-DATA: 1992US-0825665 (January 24, 1992) , 1990US-0619316 (November
28, 1990) , 1994US-0217768 (March 24, 1994) , 1995US-0383725 (February 3,
1995) , 1995US-0494954 (June 26, 1995) , 1996US-0638865 (April 29, 1996)
, 1996US-0720862 (October 2, 1996) , 1996US-0691351 (August 2, 1996)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 5809064 A	September 15, 1998	N/A	000	H04B 001/69
EP 552975 A2	July 28, 1993	E	024	H04B 007/08
AU 9331971 A	July 29, 1993	N/A	000	H04L 027/38
CA 2087909 A	July 25, 1993	N/A	000	H04L 007/03
US 5390207 A	February 14, 1995	N/A	018	G01S 005/02
CN 1080059 A	December 29, 1993	N/A	000	G01S 005/02
EP 552975 A3	May 24, 1994	N/A	000	H04B 007/08
AU 660757 B	July 6, 1995	N/A	000	H04L 027/38
US 5495499 A	February 27, 1996	N/A	020	H04L 009/00
SG 43672 A1	November 14, 1997	N/A	000	G01S 005/14
US 5734674 A	March 31, 1998	N/A	022	H04K 001/00

INT-CL: G01S005/02 G01S005/14 H04B001/16 H04B001/69 H04B007/08
H04B007/185 H04K001/00 H04K003/00 H04L007/033 H04L009/00 H04L027/30
H04L027/38

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

6. Document ID: JP 08213903 A,

Relevance Rank: 0

DERWENT-ACC-NO: 1996-430949
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Automatic VFO adjusting device - has D=A converter which converts DC
control voltage set by DC control voltage setting part into analog signal
which is input into control input terminal

Inventor Name

PRIORITY-DATA: 1995JP-0020054 (February 8, 1995)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 08213903 A	August 20, 1996	N/A	005	H03L 007/09

INT-CL: H03L007/099

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

7. Document ID: US 5767746 A JP 10117188 A,
Relevance Rank: 0

DERWENT-ACC-NO: 1998-376097
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Apparatus for phase-lock loop parameter adjustment for e.g. video systems - has successive time-stamp clock reference measurement and gain factor for feedback to new clock reference information

Inventor Name DIETERICH, C B

PRIORITY-DATA: 1996US-0660300 (June 7, 1996)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 5767746 A	June 16, 1998	N/A	008	H03L 007/19
JP 10117188 A	May 6, 1998	N/A	010	H04L 007/03

INT-CL: H03L007/107 H03L007/197 H04L007/033

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

8. Document ID: US 5808691 A,
Relevance Rank: 0

DERWENT-ACC-NO: 1998-520438
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Periodic digital signal synthesizing method for digital TV encoder - involves synchronizing periodic digital signal with reference signal, using adjustment signal

Inventor Name LUTZ, J M

PRIORITY-DATA: 1995US-0571268 (December 12, 1995)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 5808691 A	September 15, 1998	N/A	031	H04N 005/12

INT-CL: H04N005/12

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

Term	Documents
adjust and digital and synchronize	8

Display Documents

Starting At: 1

Display Format:

Change Format

Main Menu

Search Form

Posting Counts

Show WS Numbers

Edit WS Numbers

Help

WEST 1.0[Help](#)[Main Menu](#) [Search Form](#) [Posting Counts](#) [Show WS Numbers](#) [Edit WS Numbers](#)**Search Results - Record(s) 1 through 10 of 10 returned.**

1. Document ID: DE 59208599 G DE 4112141 A EP
511473 A2 EP 511473 A3 EP 511473 B1,
Relevance Rank: 99

DERWENT-ACC-NO: 1992-350643
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Wheelspin control system with starting aid - brakes one wheel of
motor vehicle when starting and gradually releases brake until wheel grips

Inventor Name SCHRAMM, H

PRIORITY-DATA: 1991DE-4112141 (April 13, 1991)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
DE 59208599 G	July 17, 1997	N/A	000	B60K 028/16
DE 4112141 A	October 15, 1992	N/A	003	B60K 028/16
EP 511473 A2	November 4, 1992	G	004	B60K 028/16
EP 511473 A3	April 13, 1994	N/A	000	B60K 028/16
EP 511473 B1	June 11, 1997	G	004	B60K 028/16

INT-CL: B60K028/16 B60T008/32

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

2. Document ID: CN 1081112 A,
Relevance Rank: 0

DERWENT-ACC-NO: 1995-155656
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Mechanical or Hydraulic driven soporiferous bed - has programmed controller moving bed vertically according to frequency curve and can either be fully elevated or partially

Inventor Name ZHU, Z

PRIORITY-DATA: 1992CN-0105360 (July 7, 1992)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
CN 1081112 A	January 26, 1994	N/A	000	A61M 021/02

INT-CL: A47C017/00 A61G007/043 A61M021/02

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

3. Document ID: DE 69412905 E EP 626562 A1 US
5541508 A EP 626562 B1,

Relevance Rank: 0

DERWENT-ACC-NO: 1995-001010
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Position detector with magnetic recording medium - has magnetic scale monitored by magnetoresistive element and processing circuit which automatically adjusts offset and dynamic range

Inventor Name SUZUKI, N

PRIORITY-DATA: 1993JP-0125327 (April 28, 1993)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
DE 69412905 E	October 8, 1998	N/A	000	G01D 003/02
EP 626562 A1	November 30, 1994	E	014	G01D 003/02
US 5541508 A	July 30, 1996	N/A	012	G01B 007/14
EP 626562 B1	September 2, 1998	E	000	G01D 003/02

INT-CL: G01B007/14 G01D001/14 G01D003/02 G01D005/16

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

4. Document ID: JP 08057079 A,

Relevance Rank: 0

DERWENT-ACC-NO: 1996-182632
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Fitness training appts - has load controller that controls amount of load applied by drive unit, based on detection value from physical condition sensor

Inventor Name

PRIORITY-DATA: 1994JP-0222615 (August 23, 1994)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 08057079 A	March 5, 1996	N/A	009	A63B 021/00

INT-CL: A63B021/00 A63B022/06 A63B023/04

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

5. Document ID: JP 08339192 A,
Relevance Rank: 0

DERWENT-ACC-NO: 1997-105043
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Active type noise control appts for vehicle - has adaptive controller which forms adjustment signal using filter coefficient corresponding to reference signal

Inventor Name

PRIORITY-DATA: 1995JP-0088209 (April 13, 1995) , 1994JP-0246344 (October 12, 1994)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 08339192 A	December 24, 1996	N/A	058	G10K 011/17

INT-CL: B60R011/02 F01N001/00 G10K011/178 G10K015/00 H03H021/00

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

6. Document ID: JP 09023128 A,
Relevance Rank: 0

DERWENT-ACC-NO: 1997-142970
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Compandor circuit with audio signal expansion/compression function for radio telephone - has control circuit to control gain of electronic volume controller and to adjust the degree of attenuation such that it synchronizes with detected amplitude signal output from detector

Inventor Name

PRIORITY-DATA: 1995JP-0169072 (July 4, 1995)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 09023128 A	January 21, 1997	N/A	012	H03G 007/00

INT-CL: H03G007/00 H04B001/40

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

7. Document ID: JP 09222022 A,
Relevance Rank: 0

DERWENT-ACC-NO: 1997-475911
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Swirl control device of engine - has swirl controller to increase/decrease amount of inlet valve lift within operating range, where opening of valve seat of inlet valve is larger than minimum sectional area of direction port

Inventor Name

PRIORITY-DATA: 1996JP-0029449 (February 16, 1996)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 09222022 A	August 26, 1997	N/A	007	F02B 031/00

INT-CL: F01L009/02 F01L013/00 F02B031/00 F02B031/02 F02D013/02

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

8. Document ID: JP 09271416 A,
Relevance Rank: 0

DERWENT-ACC-NO: 1998-003043
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Height adjustable kitchen unit for use by handicapped and aged people - has controller which controls and synchronizes lift operation of shelf and cooking unit

Inventor Name

PRIORITY-DATA: 1996JP-0108375 (April 3, 1996)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 09271416 A	October 21, 1997	N/A	009	A47B 077/04

INT-CL: A47B077/04

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

9. Document ID: JP 10210385 A,
Relevance Rank: 0

DERWENT-ACC-NO: 1998-487843
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Video display controller - selects specific control signals based on predefined positional information signals which is then output to video processor

Inventor Name

PRIORITY-DATA: 1997JP-0011003 (January 24, 1997)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 10210385 A	August 7, 1998	N/A	008	H04N 005/57

INT-CL: G09G005/10 H04N005/57 H04N005/59 H04N005/68

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

10. Document ID: JP 10319917 A,
Relevance Rank: 0

DERWENT-ACC-NO: 1999-086094
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Automatic phase controller for video signals - includes comparator
which synchronizes phase of clock signal, to video signal using two
threshold levels

Inventor Name

PRIORITY-DATA: 1997JP-0128973 (May 19, 1997)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 10319917 A	December 4, 1998	N/A	007	G09G 003/36

INT-CL: G02F001/133 G09G003/20 G09G003/36 H04N005/14

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

Term	Documents
adjust and controller and synchronize	10

Display Documents

Starting At: 1

Display Format:

Change Format

Main Menu	Search Form	Posting Counts	Show WS Numbers	Edit WS Numbers
-----------	-------------	----------------	-----------------	-----------------

Help

WEST 1.0[Help](#)[Main Menu](#) [Search Form](#) [Posting Counts](#) [Show WS Numbers](#) [Edit WS Numbers](#)

Search Results - Record(s) 1 through 6 of 6 returned.

Document ID: DE 4101270 A DE 59207187 G WO
1. 9213405 A1 AU 9211753 A EP 567517 A1 JP
06500219 W US 5459782 A EP 567517 B1,

Relevance Rank: 58

DERWENT-ACC-NO: 1992-250945
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Digital signal transmission method - having digital signals
multiplexed and timing adjusted by positive zero negative filling

Inventor Name VOLEJNIK, W

PRIORITY-DATA: 1991DE-4101270 (January 17, 1991)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
DE 4101270 A	July 23, 1992	N/A	005	H04L 005/22
DE 59207187 G	October 24, 1996	N/A	000	H04J 003/07
WO 9213405 A1	August 6, 1992	E	028	H04J 003/07
AU 9211753 A	August 27, 1992	N/A	000	H04J 003/07
EP 567517 A1	November 3, 1993	G	028	H04J 003/07
JP 06500219 W	January 6, 1994	N/A	000	H04J 003/07
US 5459782 A	October 17, 1995	N/A	014	H04L 007/00
EP 567517 B1	September 18, 1996	G	018	H04J 003/07

INT-CL: H04J003/07 H04L005/22 H04L007/00

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

Document ID: DE 69118445 E EP 462774 A JP
2. 04049781 A EP 462774 A3 US 5323237 A EP
462774 B1,

Relevance Rank: 57

DERWENT-ACC-NO: 1992-001129
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Colour television image display appts. - renders phase of video signal coincident with that of video control signal supplied to signal processor

Inventor Name ODA, O

PRIORITY-DATA: 1990JP-0158627 (June 19, 1990)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
DE 69118445 E	May 9, 1996	N/A	000	H04N 005/44
EP 462774 A	December 27, 1991	N/A	000	N/A
JP 04049781 A	February 19, 1992	N/A	005	N/A
EP 462774 A3	June 9, 1993	N/A	000	N/A
US 5323237 A	June 21, 1994	N/A	007	H04N 005/04
EP 462774 B1	April 3, 1996	E	008	H04N 005/44

INT-CL: H04N005/04 H04N005/14 H04N005/20 H04N005/44 H04N017/04

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

3.

Document ID: US 5313108 A EP 626631 A1 CA
2096469 A,

Relevance Rank: 57

DERWENT-ACC-NO: 1994-159253
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Clock signal generator producing signal stretched by one-half or one period - controls time microprocessor CPU must wait for memory access by selectively adjusting CPU clock signal depending on expected delay caused by memory access

Inventor Name KENNY, J D

PRIORITY-DATA: 1992US-0870530 (April 17, 1992) , 1993EP-0401357 (May 27, 1993) , 1993CA-2096469 (May 18, 1993)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 5313108 A	May 17, 1994	N/A	009	H03K 005/04
EP 626631 A1	November 30, 1994	E	010	G06F 001/08
CA 2096469 A	November 19, 1994	N/A	000	G06F 013/16

INT-CL: G06F001/08 G06F013/16 G06F013/42 H03K005/04

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

4.

Document ID: EP 302262 A DE 3855492 G US
5086500 A EP 302262 B1,

Relevance Rank: 46

DERWENT-ACC-NO: 1989-040448
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Reduced instruction set computer risc - uses discrete integrated circuit with constant impedance transmission lines and adjustable delayed clocks to ease heat dissipation

Inventor Name GREUB, H J

PRIORITY-DATA: 1987US-0084003 (August 7, 1987) , 1989US-0449445 (December 12, 1989)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
EP 302262 A	February 8, 1989	E	017	N/A
DE 3855492 G	October 2, 1996	N/A	000	G06F 001/04
US 5086500 A	February 4, 1992	N/A	000	N/A
EP 302262 B1	August 28, 1996	E	032	G06F 001/04

INT-CL: G06F001/04 G06F013/00 G06F015/00 H05K001/00

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

5. Document ID: DE 59108397 G EP 479268 A DE
4038561 A DE 4038561 C2 EP 479268 A3 EP
479268 B1,

Relevance Rank: 44

DERWENT-ACC-NO: 1992-116034
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Clocked correlation and signal processing system for TDM receiver - identifies required signal sequence from obtained correlation values

Inventor Name HOENIG, J

PRIORITY-DATA: 1990DE-4038561 (December 4, 1990) , 1990DE-4031124 (October 2, 1990)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
DE 59108397 G	January 16, 1997	N/A	000	H04L 007/04
EP 479268 A	April 8, 1992	N/A	025	N/A
DE 4038561 A	April 9, 1992	N/A	018	N/A
DE 4038561 C2	April 22, 1993	N/A	018	H04J 003/06
EP 479268 A3	June 9, 1993	N/A	000	N/A
EP 479268 B1	December 4, 1996	G	027	H04L 007/04

INT-CL: H04J003/06 H04L003/06 H04L007/04

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

6. Document ID: US 5365130 A,
Relevance Rank: 32

DERWENT-ACC-NO: 1994-366213
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Self-compensating output pad for integrated circuit - has latch coupled to logic output to provide clock output synchronised to edge of clock signal

Inventor Name GARINGER, N D

PRIORITY-DATA: 1992US-0926979 (August 7, 1992)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 5365130 A	November 15, 1994	N/A	007	H03K 005/13

INT-CL: H03K005/13 H03K005/26

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

Term	Documents
13 and 112	6

Display Documents

Starting At: 1

Display Format:

Change Format

Main Menu	Search Form	Posting Counts	Show WS Numbers	Edit WS Numbers
-----------	-------------	----------------	-----------------	-----------------

Help

WEST 1.0[Help](#)[Main Menu](#) [Search Form](#) [Posting Counts](#) [Show WS Numbers](#) [Edit WS Numbers](#)**Search Results - Record(s) 1 through 3 of 3 returned.**

1. Document ID: JP 08125642 A,
Relevance Rank: 42

DERWENT-ACC-NO: 1996-293786
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Delay adjusting device for digital communication system - has
quantity controller to control amount of delay of second delay part

Inventor Name

PRIORITY-DATA: 1994JP-0260403 (October 25, 1994)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 08125642 A	May 17, 1996	N/A	010	H04L 007/00

INT-CL: H04L001/22 H04L007/00

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

2. Document ID: EP 313541 A BE 1001012 A CA
1325835 C DE 3860750 G EP 313541 B ES
2018717 B US 4994975 A,
Relevance Rank: 38

DERWENT-ACC-NO: 1989-124360
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Adjustment or maintenance in register of preprinted strip -
employing periodic location of best-fit indexing pulse in sequence derived
from rotary tool position encoder

Inventor Name MINSCHART, M G

PRIORITY-DATA: 1987BE-0001190 (October 20, 1987)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
EP 313541 A	April 26, 1989	F	012	N/A
BE 1001012 A	June 13, 1989	N/A	000	N/A
CA 1325835 C	January 4, 1994	N/A	000	B65H 023/18
DE 3860750 G	November 8, 1990	N/A	000	N/A
EP 313541 B	October 3, 1990	N/A	000	N/A
ES 2018717 B	May 1, 1991	N/A	000	N/A
US 4994975 A	February 19, 1991	N/A	000	N/A

INT-CL: B41F013/02 B41F033/00 B65H023/18 B65H023/188 G06F015/46

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

Document ID: EP 48661 A BR 8105905 A CA
1198485 A DE 3169060 G EP 48661 B FR
2490427 A JP 57083907 A SU 1192643 A US
4458207 A,

Relevance Rank: 34

DERWENT-ACC-NO: 1982-D7752E
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title FM TV signal demodulator with second discriminator channel -
produces composite control signal for band-pass filter tuning using
digitised signal stored in memory

Inventor Name FAVREAU, M

PRIORITY-DATA: 1980FR-0019921 (September 16, 1980)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
EP 48661 A	March 31, 1982	F	016	N/A
BR 8105905 A	June 8, 1982	N/A	000	N/A
CA 1198485 A	December 24, 1985	N/A	000	N/A
DE 3169060 G	March 28, 1985	N/A	000	N/A
EP 48661 B	February 20, 1985	F	000	N/A
FR 2490427 A	March 19, 1982	N/A	000	N/A
JP 57083907 A	May 26, 1982	N/A	000	N/A
SU 1192643 A	November 15, 1985	N/A	000	N/A
US 4458207 A	July 3, 1984	N/A	000	N/A

INT-CL: H03D003/00 H04B001/10 H04N005/93 H04N009/50

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

Term	Documents
l2 and l12 and initial	3

Display Documents Starting At: 1

Display Format:

Main Menu	Search Form	Posting Counts	Show WS Numbers	Edit WS Numbers
-----------	-------------	----------------	-----------------	-----------------

THIS PAGE BLANK (USPTO)

WEST 1.0[Help](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show WS Numbers](#)[Edit WS Numbers](#)

Search Results - Record(s) 1 through 6 of 6 returned.

1. Document ID: EP 322308 A US 4837521 A,
Relevance Rank: 78

DERWENT-ACC-NO: 1989-186656
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Delay line control system for automatic test equipment - includes
base delay memory for controlling two counters and vernier and offset
memories serve for delaying counter output signal

Inventor Name DAVIS, J A

PRIORITY-DATA: 1987US-0135782 (December 21, 1987) , 1987US-0070130 (July 2,
1987)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
EP 322308 A	June 28, 1989	E	009	N/A
US 4837521 A	June 6, 1989	N/A	008	N/A

INT-CL: G06F011/26 G06K005/04 H03K005/13 H03K017/00

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

2. Document ID: WO 9604568 A1 AU 9533606 A DE
19581712 T,
Relevance Rank: 65

DERWENT-ACC-NO: 1996-129542
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Digital receive beam=former system for ultrasound imaging system -
uses programmable decimators to allow processing mode trade-offs among
receive frequency, receive spatial range resolution, and simultaneous sly
receive beams

Inventor Name COLE, C R

PRIORITY-DATA: 1994US-0286658 (August 5, 1994)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
WO 9604568 A1	February 15, 1996	E	079	G01S 003/80
AU 9533606 A	March 4, 1996	N/A	000	G01S 003/80
DE 19581712 T	July 24, 1997	N/A	000	G03B 042/06

INT-CL: A61B008/00 G01N029/00 G01S003/80 G03B042/06 G10K011/26

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

3. Document ID: SG 46216 A1 EP 595454 A2 US
5321559 A US 5341249 A US 5341387 A US
5345342 A US 5375145 A US 5381359 A EP
595454 A3 US 5422760 A JP 07182786 A,

Relevance Rank: 58

DERWENT-ACC-NO: 1994-146037
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Class 4 partial response max. likelihood data channel for disc drive
- conditions data samples based on selectable, adaptive FIR digital filter
coeffts. and has Viterbi detector producing class 4 code

Inventor Name ABBOTT, W L

PRIORITY-DATA: 1992US-0937352 (August 27, 1992) , 1992US-0936742 (August 27,
1992) , 1992US-0936756 (August 27, 1992) , 1992US-0936759 (August 27, 1992)
, 1992US-0936761 (August 27, 1992) , 1992US-0937064 (August 27, 1992)
, 1994US-0192146 (February 4, 1994) , 1994US-0291957 (August 17, 1994)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
SG 46216 A1	February 20, 1998	N/A	000	G11B 020/10
EP 595454 A2	May 4, 1994	E	091	G11B 020/10
US 5321559 A	June 14, 1994	N/A	029	G11B 005/09
US 5341249 A	August 23, 1994	N/A	076	G11B 005/09
US 5341387 A	August 23, 1994	N/A	020	H04L 001/00
US 5345342 A	September 6, 1994	N/A	073	G11B 005/09
US 5375145 A	December 20, 1994	N/A	022	H04L 027/08
US 5381359 A	January 10, 1995	N/A	025	G06F 007/38
EP 595454 A3	June 15, 1994	N/A	000	G11B 020/10
US 5422760 A	June 6, 1995	N/A	072	G11B 005/09
JP 07182786 A	July 21, 1995	N/A	062	G11B 020/10

INT-CL: G06F007/38 G06J001/00 G11B005/035 G11B005/09 G11B005/596
G11B020/10 G11B020/18 G11B027/10 H03H007/30 H04L001/00 H04L027/08

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

4. Document ID: DE 4135630 A DE 4143349 C2 FR
2669751 A1 DE 4143349 A DE 4143348 A DE
4143351 A DE 4143350 A US 5208598 A US
5224129 A US 5249132 A US 5252977 A JP
06090148 A DE 4143350 C2 DE 4143348 C2 DE
4143351 C2 DE 4135630 C2 US 5430660 A,

Relevance Rank: 57

DERWENT-ACC-NO: 1992-168258
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Digital pulse generator for digital synthesis - uses triggered voltage=con trolled oscillator providing time base signal modified via stored digital pattern

Inventor Name HENGEVELD, J

PRIORITY-DATA: 1990US-0606387 (October 31, 1990) , 1992US-0848638 (March 9, 1992) , 1992US-0848637 (March 9, 1992) , 1992US-0848609 (March 9, 1992) , 1993US-0069329 (June 1, 1993)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
DE 4135630 A	May 14, 1992	N/A	026	H03K 003/00
DE 4143349 C2	May 9, 1996	N/A	023	G01R 035/00
FR 2669751 A1	May 29, 1992	N/A	000	G06F 001/02
DE 4143349 A	November 5, 1992	N/A	000	H03K 003/00
DE 4143348 A	November 12, 1992	N/A	000	H03K 003/00
DE 4143351 A	November 12, 1992	N/A	000	H03K 003/00
DE 4143350 A	November 19, 1992	N/A	000	H03K 003/00
US 5208598 A	May 4, 1993	N/A	021	H03K 005/04
US 5224129 A	June 29, 1993	N/A	019	H04L 007/04
US 5249132 A	September 28, 1993	N/A	020	G06F 015/20
US 5252977 A	October 12, 1993	N/A	021	H03K 005/04
JP 06090148 A	March 29, 1994	N/A	018	H03K 005/13
DE 4143350 C2	August 25, 1994	N/A	026	H03K 003/00
DE 4143348 C2	September 22, 1994	N/A	023	H03K 003/00
DE 4143351 C2	October 20, 1994	N/A	022	H03K 003/00
DE 4135630 C2	January 5, 1995	N/A	022	H03K 003/00
US 5430660 A	July 4, 1995	N/A	021	G06F 015/20

INT-CL: G01R013/20 G01R035/00 G06F001/02 G06F015/20 H03K003/00
H03K003/64 H03K005/00 H03K005/04 H03K005/13 H03K005/135 H03L007/00
H04L007/04

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

5. Document ID: US 4445114 A,
Relevance Rank: 47

DERWENT-ACC-NO: 1984-120756
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Appts. scrolling video display - has ROM containing pictorial video data representative objects and generates scroll index

Inventor Name STUBBEN, D R

PRIORITY-DATA: 1980US-0193699 (October 3, 1980) , 1979US-0003447 (January 15, 1979)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 4445114 A	April 24, 1984	N/A	000	N/A

INT-CL: G09G001/16

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

6. Document ID: US 5028878 A,
Relevance Rank: 37

DERWENT-ACC-NO: 1991-215257
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Dual memory timing system for VLSI test systems - uses shared address generator to address memories that form basis of each pin timing generator

Inventor Name CARLSON, M E

PRIORITY-DATA: 1989US-0435127 (November 13, 1989)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 5028878 A	July 2, 1991	N/A	000	N/A

INT-CL: G01R031/28 H03L007/00

Full	Citation	Review	Classification	Date	Reference
------	----------	--------	----------------	------	-----------

Term	Documents
l2 and vernier and digital	6

Display Documents

Starting At: 1

Display Format: Change Format

Main Menu	Search Form	Posting Counts	Show WS Numbers	Edit WS Numbers
-----------	-------------	----------------	-----------------	-----------------

Help

WEST 1.0[Help](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show WS Numbers](#)[Edit WS Numbers](#)

Search Results - Record(s) 1 through 1 of 1 returned.

1. Document ID: SG 46216 A1 EP 595454 A2 US
5321559 A US 5341249 A US 5341387 A US
5345342 A US 5375145 A US 5381359 A EP
595454 A3 US 5422760 A JP 07182786 A,

Relevance Rank: 49

DERWENT-ACC-NO: 1994-146037
COPYRIGHT 1998 DERWENT INFORMATION LTD

Title Class 4 partial response max. likelihood data channel for disc drive
- conditions data samples based on selectable, adaptive FIR digital filter
coeffts. and has Viterbi detector producing class 4 code

Inventor Name ABBOTT, W L

PRIORITY-DATA: 1992US-0937352 (August 27, 1992) , 1992US-0936742 (August 27,
1992) , 1992US-0936756 (August 27, 1992) , 1992US-0936759 (August 27, 1992)
, 1992US-0936761 (August 27, 1992) , 1992US-0937064 (August 27, 1992)
, 1994US-0192146 (February 4, 1994) , 1994US-0291957 (August 17, 1994)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
SG 46216 A1	February 20, 1998	N/A	000	G11B 020/10
EP 595454 A2	May 4, 1994	E	091	G11B 020/10
US 5321559 A	June 14, 1994	N/A	029	G11B 005/09
US 5341249 A	August 23, 1994	N/A	076	G11B 005/09
US 5341387 A	August 23, 1994	N/A	020	H04L 001/00
US 5345342 A	September 6, 1994	N/A	073	G11B 005/09
US 5375145 A	December 20, 1994	N/A	022	H04L 027/08
US 5381359 A	January 10, 1995	N/A	025	G06F 007/38
EP 595454 A3	June 15, 1994	N/A	000	G11B 020/10
US 5422760 A	June 6, 1995	N/A	072	G11B 005/09
JP 07182786 A	July 21, 1995	N/A	062	G11B 020/10

INT-CL: G06F007/38 G06J001/00 G11B005/035 G11B005/09 G11B005/596
G11B020/10 G11B020/18 G11B027/10 H03H007/30 H04L001/00 H04L027/08

Full	Citation	Review	Classification	Date	Reference
----------------------	--------------------------	------------------------	--------------------------------	----------------------	---------------------------

Term	Documents
I2 and vernier and digital and controller	1

Display Documents

Starting At: 1

Display Format:

Change Format

Main Menu

Search Form

Posting Counts

Show WS Numbers

Edit WS Numbers

Help